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FIG 1

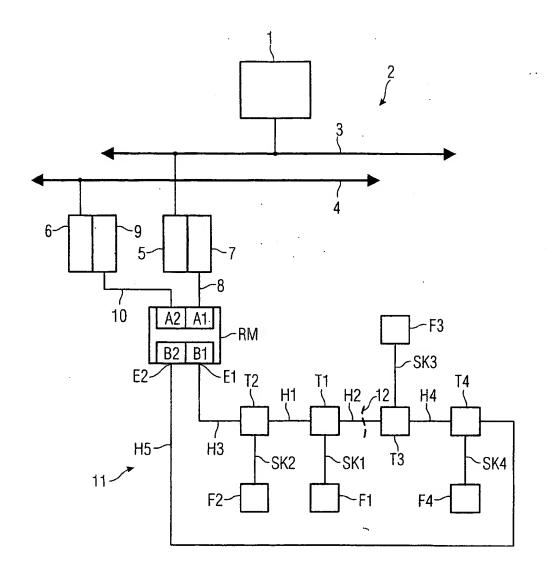


FIG 2

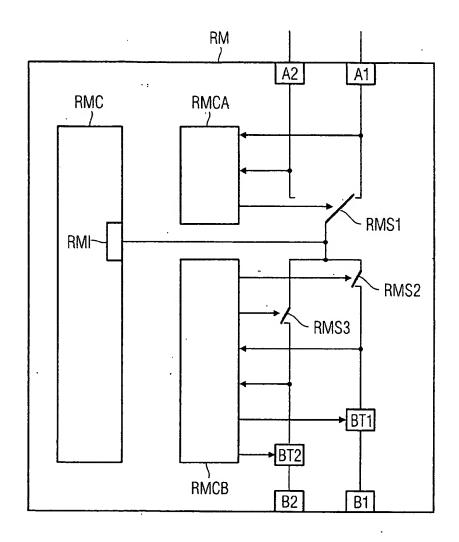
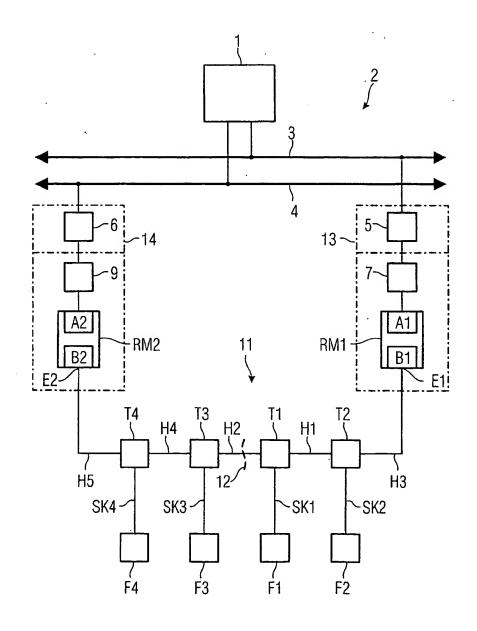
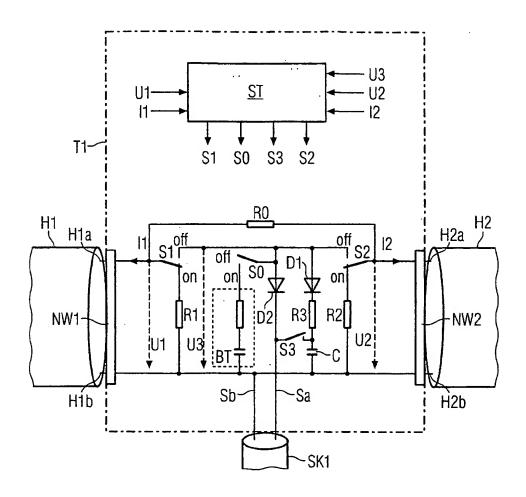


FIG 3

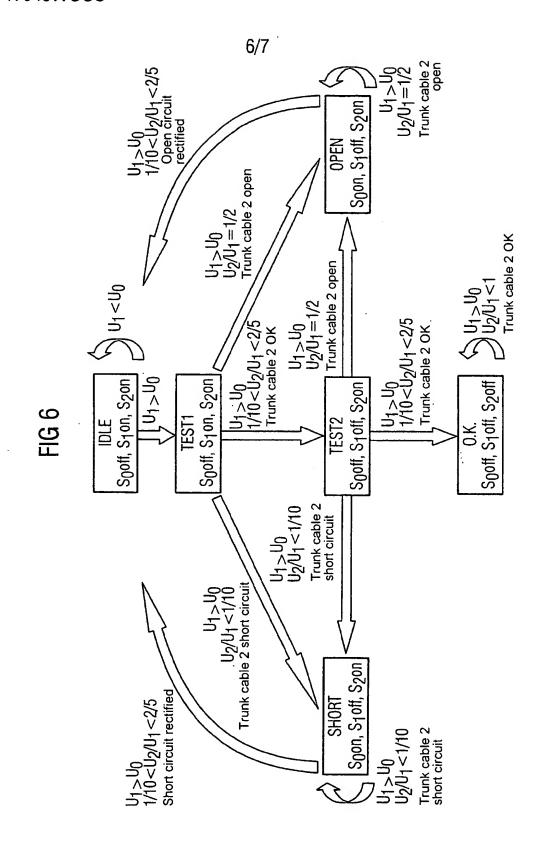


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FIG 4



									J) I										
Remarks			Trunk cable 2 OK	Trunk cable 2 short circuited	Trunk cable 2 open circuit	Feed also via trunk cable 2	Trunk cable 2 OK	Trunk cable 2 short circuited	Trunk cable 2 open circuit	Trunk cable 2 short circuited	RESET from RM	Short circuit rectified	Trunk cable 2 open circuit		RESET from RM	Open circuit rectified	Trunk cable 2 short circuited		RESET from RM
Next state	IDLE	TEST1	TEST2	SHORT	OPEN .	OK	0K	SHORT	OPEN	SHORT	IDLE	IDLE	OPEN	OPEN	IDLE	IDLE	SHORT	SO	IDLE .
S2	00	00	uo				uo			uo				ПO				JJO	
S1	00	uo	uo				IJО			jjo				#0				JJO	
S0	JJO	JJO	ЩO				IJО			uo				U0				off	
K=U2/U1			<2/5 ≥1/10	<1/10	≈1/2	>1/2	≤2/5 ≥1/10	<1/10	≈1/2	<1/10		<2/5 ≥1/10	≈1/2	≈1 <u>/</u> 2		<2/5 >1/10	<1/10	*	1
UI	<u0< td=""><td>&gt;00</td><td>on&lt;</td><td>on&lt;</td><td>&gt; 00</td><td>on&lt;</td><td>on&lt;</td><td>on &lt;</td><td>&gt;00</td><td>on&lt;</td><td>&gt; N0</td><td>on &lt;</td><td>&gt;00</td><td>) ^</td><td>0N&gt;</td><td>on&lt;</td><td>on&lt;</td><td>on&lt;</td><td>on&gt;</td></u0<>	>00	on<	on<	> 00	on<	on<	on <	>00	on<	> N0	on <	>00	) ^	0N>	on<	on<	on<	on>
State	E		TEST1			TEST2			SHORT				OPEN				Ж		



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FIG 7

